	EAST History		11/9/04		3:21 PM
BRS	L1	1345	RAM adj cells	USPAT	·
BRS	L2	955	Random adj Access adj Memory adj cells	USPAT	
BRS	L3	2185	1 or 2	USPAT	
BRS	L4	184	3 and FPGA	USPAT	
BRS	L5	150	3 and (Field adj Programmable adj Gate adj Array)	USPAT	
BRS	L6	212	4 or 5 .	USPAT	
BRS	L7	104	6 and decoder	USPAT	